

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yasunobu TAGUSA	Art Unit: 1792
Application No.: 10/598,488	
Confirmation No.: 6247	Examiner: F. Hiteshew
Filing or 371(c) Date: August 31, 2006	
Title: METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE AND APPARATUS FOR INSPECTING A SEMICONDUCTOR	

**RESPONSE TO RESTRICTION REQUIREMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement issued on April 2, 2009, Applicant elects Group I, including claims 23-39 for prosecution in the present application. This election is made without traverse.

Applicant hereby reserves the right to file a Divisional Application to pursue prosecution of the non-elected claims.

Favorable consideration and prompt allowance of the present application are respectfully requested.

April 23, 2009

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The Commissioner is authorized to charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 50-1353.

Respectfully submitted,

Dated: April 23, 2009

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